APCCAS 2014
2014 IEEE Asia Pacific Conference on Circuits and Systems
Program Overview

Monday, 17 Nov. 2014

	Ballroom Maesato		
	(1)	(2)	(3)
13:00-18:00	Registration at Maesato Foyer		
14:30-17:30	Tutorial (T1)	Tutorial (T2)	Tutorial (T3)
18:00-19:30	Welcome Reception at Garden		

Tuesday, 18 Nov. 2014

	Ballroom Maesato			Top of Yaima Posters/Workshop	Others
	(1)	(2)	(3)		
8:00-18:00	Registration at Maesato Foyer				
9:00-9:15	Opening Ceremony				
9:15-10:15	Keynote Speech I (Prof. Takeo Kanade)				
10:15-10:30	Coffee Break at Maesato Foyer				
10:30-11:30	Keynote Speech II (Prof. Thanos Stouraitis)				
11:30-13:00	Lunch Break (Box Lunches are available at Maesato Foyer)				WiCAS Lunch (11:45-12:45) at Fireside Terrace
13:00-14:40	A1L-A System-on-Chip	A1L-B Image Processing and Coding	A1L-C Analog Signal Processing	A3P-D Embedded Systems, Circuits and Systems for Communications	A3P-E Live Demonstration (13:00-15:30) at Maesato Foyer
14:40-15:00	Coffee Break at Maesato Foyer				
15:00-16:40	A2L-A VLSI Systems & Applications I (Systems for Image and Signal Processing)	A2L-B Speech/Sound Signal Processing	A2L-C Voltage and Current Regulator	A4P-D Analog Signal Processing, RF building blocks, Nonlinear, Biomedical Circuits and Systems	

Wednesday, 19 Nov. 2014

	Ballroom Maesato			Top of Yaima Posters/Workshop	Others
	(1)	(2)	(3)		
8:00-18:00	Registration at Maesato Foyer				
9:00-10:40	B1L-A VLSI Systems & Applications II (High-Performance and Security)	B1L-B Multimedia Systems and Applications	B1L-C Switching Converters and Applications	B5P-D Digital Signal Processing, Multimedia Systems and its Applications	International Steering Committee Meeting of APCCAS and PrimeAsia (9:00-12:00) at Ballroom Panari
10:40-11:00	Coffee Break at Maesato Foyer				
11:00-12:40	B2L-A VLSI Systems & Applications III (Timing and Variation Aware Systems)	B2L-B Image Sensor Based Visible Light Communication	B2L-C RF Building Blocks	B6P-D Sensor Systems, Emerging Memory Technology, Power Converters, Emerging Circuits & Systems	
12:40-14:00	Lunch Break (Box Lunches are available at Maesato Foyer)				
14:00-15:40	B3L-A Embedded Systems	B3L-B Systems and Networks for Safe and Secure Life	B3L-C Biomedical Circuits and Systems	B7P-D VLSI Systems & Applications and System-on-Chip	
15:40-16:00	Coffee Break at Maesato Foyer				
16:00-17:40	B4L-A Circuits and Systems for Communications 1	B4L-B Recent Advance on Signal Processing for Audio and Acoustic System	B4L-C Sensor Systems and Emerging Memory Technology	CAS Conference Leadership Workshop by CASS VP-Conferences (16:00-18:00)	
19:00-21:00	Banquet at Ballroom Maesato				

Thursday, 20 Nov. 2014

	Ballroom Maesato		
	(1)	(2)	(3)
8:30-10:40	Registration at Maesato Foyer		
9:00-10:40	C1L-A Circuits and Systems for Communications 2	C1L-B Technologies for Innovative Multi-Core NoC	C1L-C Emerging Circuits and Systems Technologies and Techniques
10:40-11:00	Coffee Break at Maesato Foyer		
11:00-12:40	C2L-A Circuits and Systems for Communications 3	C2L-B PostSilicon Adaptation for Performance, Reliability and Yield	C2L-C Nonlinear Circuits and Neural Systems
13:00-17:00	Post Conference Meeting		

Special Sessions
B2L-B, B3L-B, B4L-B, C1L-B, C2L-B

Poster Sessions
A3P-D, A4P-D, B5P-D, B6P-D, B7P-D

Live Demostration Session
A3P-E

All Other Sessions Are Lecture Sessions